

<b>L Number</b>	<b>Hits</b>	<b>Search Text</b>	<b>DB</b>	<b>Time stamp</b>
<b>1</b>	<b>3180</b>	<b>CMOS and (non adj1 volatile adj1 memory)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 17:35</b>
<b>2</b>	<b>1315</b>	<b>(CMOS and (non adj1 volatile adj1 memory)) and substrate</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:16</b>
<b>3</b>	<b>718</b>	<b>((CMOS and (non adj1 volatile adj1 memory)) and substrate) and (floating adj1 gate)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:16</b>
<b>4</b>	<b>25</b>	<b>((((CMOS and (non adj1 volatile adj1 memory)) and substrate) and (floating adj1 gate)) and ((block\$3 or prevent\$3) adj5 light)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:37</b>
<b>5</b>	<b>129</b>	<b>(CMOS and (non adj1 volatile adj1 memory)) and ((block\$3 or prevent\$3) adj5 light)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:44</b>
<b>6</b>	<b>0</b>	<b>(CMOS and (non adj1 volatile adj1 memory)) and (camera same (on adj1 chip))</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:45</b>
<b>7</b>	<b>300</b>	<b>(CMOS and (non adj1 volatile adj1 memory)) and camera</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:45</b>
<b>8</b>	<b>11</b>	<b>((CMOS and (non adj1 volatile adj1 memory)) and camera ) and polyamide</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:50</b>
<b>9</b>	<b>0</b>	<b>polyamide with (non adj1 volatile adj1 memory)</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 13:51</b>
<b>10</b>	<b>30</b>	<b>(CMOS and (non adj1 volatile adj1 memory)) and polyamide</b>	<b>USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB</b>	<b>2002/08/19 14:29</b>

11	0	camera and (on adj1 chip)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 14:30
12	15670	camera and chip	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 14:42
13	2034	(camera and chip) and 348/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 14:31
14	249	((camera and chip) and 348/\$.ccls.) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 14:31
15	263	camera same chip same CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 14:43
16	176	(camera same chip same CMOS) and substrate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 15:04
17	109	(camera same chip same CMOS) and ((block\$3 or prevent\$3) same light)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 15:58
18	15648	((light adj1 shield) or mask ) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 16:02
19	151	((((light adj1 shield) or mask ) and CMOS) and 348/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 16:39
20	4	("4354104"   "4369372"   "4583002"   "5841126").PN.	USPAT	2002/08/19 16:17
21	3895	polyamide with light	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 16:46

22	0	(polyamide with light) with CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 16:40
23	0	(polyamide with light) same CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 16:40
24	13	(polyamide with light) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 16:40
25	32663	polyamide with material	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 16:47
26	185	(polyamide with material) and CMOS	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:07
27	0	(polyamide with material) and (CMOS adj1 image adj1 sensor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:08
28	618	(CMOS adj1 image adj1 sensor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:10
29	1	((CMOS adj1 image adj1 sensor)) and polyamide	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:08
30	809	(CMOS adj1 image adj1 sensor) or (CMOS adj1 imager)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:11
31	53	((CMOS adj1 image adj1 sensor) or (CMOS adj1 imager)) and opaque	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:11

32	2986	non adj1 volatile adj1 memory adj1 cell\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:38
33	0	( (CMOS adj1 image adj1 sensor) or (CMOS adj1 imager)) and (non adj1 volatile adj1 memory adj1 cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:39
34	4	(non adj1 volatile adj1 memory adj1 cell\$1) and (protective adj1 material)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/08/19 17:42
35	7	("5466622"   "5506159"   "5541129"   "5553017"   "5792696"   "5889305"   "6025228").PN.	USPAT	2002/08/19 17:44